UTILITY PATENT APPLICATION TRANSMITTAL

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Customer N . 23696 Att rney Docket N .: 030439 Date: December 31, 2003

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Dear	Sir:				·	
	Transmitted herewith for filing is the patent application of: Inventor(s): Jagrut V. Patel, Martin V. Choe and Ziad Mansour For: IDENTIFYING PROCESS AND TEMPERATURE OF SILICON CHIPS					
1. 2 2 3 4 5 6 7	Declaration/Power This application cl An Assignment (1 A Preliminary Am Information Discloration	mal () sheet(s) or of Attorney: laims priority from A) pages and Recordatendment () pages Statement (ID)		gned , Filed , Status	.	
	CLAIMS:	(a) Filed	(b) Extra Claims	Large Entity Fee	Fee Paid	
	Total*	32 - 20	12	x \$18 =	\$576.00	
	Independent**	4 - 3	1	x \$86 =	\$86.00	
	Multiple Dependent Claim(s): No Yes			\$280	\$0	
	APP	LICATION FILING	\$770	\$770.00		
,		n a is less than 20, enter 0 nn b is less then 3, enter 0	TOTAL FEE	\$1,432.00		
 9. A check in the amount of \$ is enclosed to pay the filing fee. 10. Please charge Deposit Account No. 17-0026 of QUALCOMM Incorporated the amount of \$1,432.00. The Commissioner is hereby authorized to charge payment of any additional fees which may be required, or credit any overpayment, to said Deposit Account No. 17-0026. A duplicate of this sheet is enclosed for fee processing. 11. The Commissioner is further hereby authorized to charge to said Deposit Account No. 17-0026, pursuant to 37 CFR 1.25(b), any fee whatsoever which may become properly due or payable, as set forth in 37 CFR 1.16 to 37 CFR 1.18 inclusive, for the entire pendency of this application without specific additional authorization. 						
Date	Date: December 31, 2003 Signature: Nicholas J. Pauley, Reg. No. 44,999- Phone No. (858) 845-8465					

CORRESPONDENCE ADDRESS

Send correspondence to Customer No. 23696 at the following address:

QUALCOMM Incorporated Attn: Patent Department 5775 Morehouse Drive

San Diego, California 92121-1714 Telephone: (858) 845-8405 Facsimile:

(858) 658-2502